

L Number	Hits	Search Text	DB	Time stamp
-	17872	(Photobit OR Micron).AS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 16:14
-	14271	(Photobit OR "Micron Technology").AS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 16:15
-	14270	("Photobit Corp" OR "Photobit Corporation" OR "Micron Technology").AS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 16:17
-	8	((("Photobit Corp" OR "Photobit Corporation" OR "Micron Technology").AS.) AND CMOS.CLM. AND digital.CLM.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 16:19
-	32	@ad<19981029 AND (348/\$).CCLS. AND cmos AND integrat\$3 AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital NEAR3 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/28 10:02
-	7	("4363963" "4710817" "4929913" "5225696" "5236871" "5306905" "5461425").PN.	USPAT	2002/06/27 16:40
-	18	5665959.URPN.	USPAT	2002/06/28 15:11
-	17	@ad<19981029 AND (348/\$).CCLS. AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (integrat\$3 WITH memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 18:07
-	477	@ad<19981029 AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (integrat\$3 WITH memory) NOT (348/\$).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 16:58
-	43	@ad<19981029 AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (integrat\$3 WITH memory) AND (imager OR "image sensor" OR "image pickup" OR "image pick up" OR "imaging device") NOT (348/\$).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 17:00
-	2	5665959.PN. AND integrat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 17:29
-	9	"4710817"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 17:29
-	31	@ad<19981029 AND (348/\$).CCLS. AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital)) AND (integrat\$3 WITH memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 18:15
-	3	("5471515").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/27 18:15
-	1	@ad<19981029 AND (348/\$).CCLS. AND cmos AND (photon NEAR7 count\$3) AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital NEAR3 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/06/28 10:04

-	1	@ad<19981029 AND (348/\$).CCLS. AND cmos AND (photon NEAR7 count\$3) AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital SAME memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 10:05
-	5	@ad<19981029 AND (348/\$).CCLS. AND cmos AND (photon NEAR7 accumulat\$3) AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital SAME memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 10:35
-	97	@ad<19981029 AND (348/231 OR 348/241 OR 348/308).CCLS. AND ("a/d" OR adc OR (analog ADJ2 digital)) AND ("complementary metal" OR mos OR cmos)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 13:25
-	2	@ad<19981029 AND (348/\$).CCLS. AND cmos AND (photon NEAR7 total\$2) AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital SAME memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 12:54
-	30	@ad<19981029 AND (348/297 OR 348/298).CCLS. AND ("a/d" OR adc OR (analog ADJ2 digital)) AND ("complementary metal" OR mos OR cmos)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 13:29
-	33	@ad<19981029 AND (348/297 OR 348/298).CCLS. AND ((count\$3 OR total\$3 OR add\$2 OR accumulat\$3) NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 13:38
-	15	@ad<19981029 AND (348/297 OR 348/298).CCLS. AND (integrat\$3 NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 14:41
-	5	@ad<19981029 AND (348/297 OR 348/298).CCLS. AND cmos AND noise AND ("a/d" OR adc OR (analog ADJ2 digital))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 16:48
-	3	("5665959").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 15:44
-	114	(348/\$).CCLS. AND "active pixel sensor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 15:47
-	3	("5841126").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 15:49
-	3	("5,471,515").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 15:49
-	3	5461425.PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 16:43
-	6	@ad<19981029 AND (348/297 OR 348/298).CCLS. AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/06/28 17:07

-	7	@ad<19981029 AND (348/\$).CCLS. AND cmos AND ("a/d" OR adc OR (analog ADJ2 digital)) AND oversampl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 09:34
-	10	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (integrat\$3 NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 12:22
-	0	5828408.URPN.	USPAT	2002/07/01 09:56
-	10	("3808435" "4054797" "4609823" "4695889" "4728804" "4744057" "4831621" "5014131" "5043924" "5315114").PN.	USPAT	2002/07/01 09:56
-	0	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (integrat\$3 NEAR9 (ram OR dram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 10:07
-	14	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (integrat\$3 NEAR9 storage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 12:36
-	18	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (integrat\$3 NEAR9 digital\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 12:43
-	2	6115065.URPN.	USPAT	2002/07/01 10:35
-	19	("4589024" "4626916" "4706123" "4819070" "4821103" "4884142" "4959723" "5084704" "5162914" "5262871" "5264944" "5274459" "5434619" "5471515" "5485206" "5541402" "5541654" "5576763" "5585847").PN.	USPAT	2002/07/01 10:35
-	26	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (digital NEAR9 (memory OR ram OR dram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 11:14
-	4	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (digital NEAR9 (add\$2 OR accumulat\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 12:36
-	21	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos AND (integrat\$3 SAME memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 10:20
-	37	@ad<19981029 AND (348/\$).CCLS. AND cmos AND ((integrat\$3 NOT "integrated circuit") SAME memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:33
-	5	5892541.URPN.	USPAT	2002/07/01 13:34
-	3	("4710817").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:29
-	4	4710817.URPN.	USPAT	2002/07/01 14:27
-	3	("4592029" "4638354" "4639781").PN.	USPAT	2002/07/01 14:27
-	66	Ando-Fumihiko.IN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:33

-	0	Ando-Fumihiko.IN. AND cmos	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:33
-	8	@ad<19981029 AND (348/\$).CCLS. AND cmos AND oversamp1\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:43
-	3	("5471515").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/01 14:43
-	26	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (("A/D" OR adc OR "analog to digital") NEAR3 pixel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 08:35
-	3	("5461425").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 08:33
-	97	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (integrat\$3 NEAR7 (memory OR ram OR storage))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 09:40
-	14	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (integrat\$3 NEAR7 (adder OR counter))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 09:38
-	14	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (integrat\$3 NEAR7 sum\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 09:39
-	6366	@ad<19981029 AND (integrat\$3 NEAR7 (memory OR ram OR storage)) AND cmos	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 09:43
-	1153	@ad<19981029 AND (integrat\$3 NEAR7 (memory OR ram OR storage)) AND cmos AND (image OR camera)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 09:46
-	110	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND cmos	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 10:22
-	373	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (cmos OR "metal oxide" OR mos)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 10:23
-	110	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (cmos OR "complementary metal")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 14:58
-	3	("5471151").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/07/02 11:02
-	2	6115065.URPN.	USPAT	2002/07/02 12:53

-	19	("4589024" "4626916" "4706123" "4819070" "4821103" "4884142" "4959723" "5084704" "5162914" "5262871" "5264944" "5274459" "5434619" "5471515" "5485206" "5541402" "5541654" "5576763" "5585847").PN.	USPAT	2002/07/02 12:53
-	4	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (integrat\$3 NEAR4 outside)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 14:24
-	21	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND ((integrat\$3 NEAR7 memory) SAME digital)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 14:25
-	412	Kondo-Makoto.IN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 14:32
-	15	Kondo-Makoto.IN. AND 348/\$.CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 14:57
-	13	Kondo-Makoto.IN. AND 348/\$.CCLS. AND (digital OR "A/D" OR adc)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 14:58
-	72	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (cmos OR "complementary metal") AND (digital OR "A/D" OR adc)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 15:42
-	66	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (integra\$4 NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 11:21
-	3	("4710817").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 16:20
-	4	4710817.URPN.	USPAT	2002/07/02 16:21
-	5	@ad<19981029 AND (348/296).CCLS. AND (integra\$4 NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 16:54
-	19	@ad<19981029 AND (348/294-332).CCLS. AND cmos AND (integra\$4 NEAR9 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 16:55
-	0	"8707933".PN.	USPAT; US-PGPUB	2002/07/02 17:13
-	4	("7807933").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 17:14
-	2	("6002432").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/02 17:16

-	32	@ad<19981029 AND (348/\$).CCLS. AND cmos AND integrat\$3 AND ("a/d" OR adc OR (analog ADJ2 digital)) AND noise AND (digital NEAR3 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 11:22
-	3	("5892541").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 09:00
-	5	5892541.URPN.	USPAT	2002/07/03 09:00
-	5	("4335405" "4734583" "5144447" "5541654" "5608243" "8707933").PN.	USPAT	2002/07/03 09:05
-	34	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND substrate AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 11:41
-	32	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (substrate OR "integrated circuit" OR ic) AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND (memory SAME (substrate OR "integrated circuit" OR ic))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 12:33
-	4	@ad<19981029 AND (348/231 OR 348/241 OR 348/308 OR 348/297 OR 348/298).CCLS. AND (substrate OR "integrated circuit" OR ic) AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND (memory SAME (substrate OR "integrated circuit" OR ic)) AND cmos	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 11:46
-	21	@ad<19981029 AND (348/\$).CCLS. AND (substrate OR "integrated circuit" OR ic) AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND ((memory OR ram) SAME (substrate OR "integrated circuit" OR ic)) AND cmos	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 13:52
-	21	@ad<19981029 AND (348/294-332).CCLS. AND (substrate OR "integrated circuit" OR ic) AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND ((memory OR ram) SAME (substrate OR "integrated circuit" OR ic))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 13:59
-	32	@ad<19981029 AND (348/294-332).CCLS. AND (substrate OR chip) AND (a/d OR adc OR "analog to digital") AND (noise OR cds) AND ((memory OR ram) SAME (substrate OR chip))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 14:37
-	6	@ad<19981029 AND (348/294-332).CCLS. AND oversampl\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/07/03 14:37